VISHNU
UNIVERSAL LEARNING
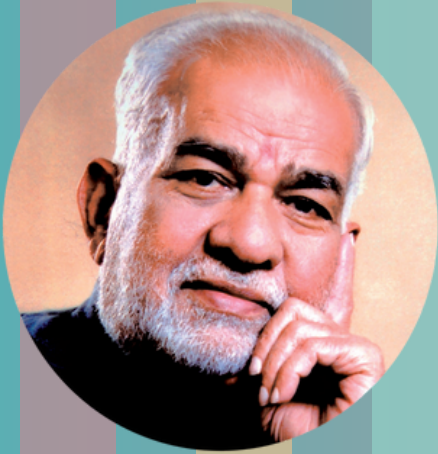
3rd ISSUE
MARCH 2022

ENCYNOSURE
Let's Excel...

VISHNU (Autonomous)
Institute of Technology

VISHNU INSTITUTE OF TECHNOLOGY

Padma Bhushan
Dr. B. V. Raju

Vishnu Institute of Technology, the scion of Shri Vishnu Educational Society was established in 2008 and is currently the eleventh educational institution to disseminate education under the aegis of this society. Nascent that it is, it combines in its matrix the lofty idealism of its Founder Chairman the Late Padmabhushan Dr. B. V. Raju, a distinguished industrialist, philanthropist and an eminent educationalist; the experience and vigour provided by the Chairman Sri K.V.Vishnu Raju, a man of holistic vision and his team comprising dexterous administrators, reputed academicians and brilliant line of students. They constantly strive to make the institution join the ranks of prestigious technical institutions.

Campus

The Campus, sprawling over 100 acres, is located in the verdant atmosphere of Vishnupur, Bhimavaram. It is in the vicinity of the town and is well connected by rail and road. VISHNU provides a home away from home to students who opt for a residential mode of education. The hostels are amidst the serene and picturesque green ambience congenial to pursue education.

Hands on Experience

To make the instruction in VISHNU more practical-oriented, special focus is on hands on experience. The Assistive Technologies Lab works in collaboration with the University of Massachusetts, USA to help students combine technology with a humane approach.

Gadgets for the physically challenged are designed and developed here by students under the guidance of eminent professors both from the Institute and abroad. VISHNU aims at empowering students with technical skills and a "can-do" entrepreneurial spirit. The IBM Software Centre of Excellence in the campus provides students with World-Class education there by increasing the skill set of each student and faculty for a great career.

HELM OF AFFAIRS

Academics are a continuing process of exploration, growth and sustenance. Today information explosion has brought about many changes. Every day brings in new demands. One has to constantly upgrade to cope with the fast emerging trends. Hence it has become imperative to all the stakeholders in education to strengthen themselves with the necessary knowledge, skills and attitude to keep themselves abreast of the rapid changes.

The stimulus has been given...

it is time for your response.

Sri K. V. Vishnu Raju
Chairman, SVES

Sri Ravichandran Rajagopal
Vice Chairman, SVES

The way I want to inspire as a leader is to keep the faculty focused on the objective of the business of education itself and to keep my students engaged in a meaningful way and not only through the regular academic rigor of running a syllabus but also to identify their inner talent. Therefore it is as much an important attribute that we need to bring into our students in our college and create career track paths through interactions with them. My job is to bring that experience by giving them the right alignment tools to accomplish their goals.

I come with deep insights into quality education in the areas of future skills required to be embedded in the courses offered by our autonomous colleges across Sri Vishnu institutions, and industry relevant knowledge to drive disruptions in higher education. I'm in close coordination with the Vishnu Educational Development and Innovation Centre (**VEDIC**) at Hyderabad, a campus dedicated to innovations in teaching pedagogy, learning styles and continuous assessment methods for active learning of students.

Sri. K. Aditya Vissam
Secretary, SVES

Along with reaching the pinnacle of academic excellence, we at Vishnu Institute of Technology aim for our students to fill their lives with positivity, hope, and happiness. We motivate them to acquire values and skills that they can rely upon in leading their lives purposefully as individuals and global citizens. In turn, we promise to provide an enabling environment and careful nurturing and honing of their talents through academic and co-curricular explosure. I applaud the efforts of our faculty for their hardwork and also commitment of our parents for having invested their energy and time in helping our students realize their dreams. I wish the Vishnu Institute of Technology to have great success in all its efforts

Dr.D.Suryanarayana
Director & Principal

Our priority is to provide every one with formal qualification for a rewarding career. In Vishnu, we aim for every student to settle in a suitable career path of choice. Together with the entire supportive community of vishnu's dedicated faculty and staff, we will do our best to provide you with a caring environment that will enable you to take the right steps towards a brighter future and career success, as well as outstanding student experiences. We pride ourselves in timely completion by students. I therefore wish to urge you to work hard from the very beginning and remain steadfast.

Prof.K.Srinivas
Vice Principal

With immense pleasure, I with my faculty team play a vital role in leading the ECE Department of Vishnu Institute of Technology towards development with growing pace of technology. Department magazine **EnCynosurE** is one such part of initiative, which empowers our students towards overall development of one's self, department and the organization. Through this platform I would like to congratulate the VIT-ECE for continuing the bimonthly department magazine **EnCynosurE.** I'm sure this technical culture will be a platform to both student and faculty fraternity to exhibit the hidden talents. Finally, my best wishes to the editorial board members and huge applause to the contributors for taking the responsibility and making this happen.

Dr. N.Padmavathy
Head of the Department

VISION OF THE COLLEGE

To empower the students through Academic excellence and Ethics so as to bring about social transformation and prosperity.

MISSION OF THE COLLEGE

- To expand the frontiers of knowledge through quality education.
- To provide value added Research and development.
- To embody a spirit of excellence in Teaching, Creativity, Entrepreneurship and Outreach.
- To provide a platform for synergy of Academy, Industry and Community.
- To inculcate high standards of Ethical and Professional behaviour.

VISION OF THE DEPARTMENT

In pursuit of world class excellence in the field of Electronics and Communication Engineering by empowering quality education and research.

MISSION OF THE DEPARTMENT

- To empower the students with knowledge and competencies in the frontier fields conforming to international standards.
- To enable the students to develop innovative solutions in collaboration with industries and research institutes to meet the local and global needs through project based learning.
- To mould the students professionally with a consciousness of moral values and professional ethical code.

About the institute

Vishnu Institute of Technology, was established in 2008 by Sri Vishnu Educational Society (SVES), a leading educational trust to promote quality education, under the aegis of eminent philanthropist Padma Bhushan Dr. B.V.Raju (Late). The institute is approved by AICTE and is permanently affiliated to JNTUK, Kakinada. It has got Autonomous status in the academic year 2019-20 and is accredited by NBA and NAAC 'A+' with 3.51 CGPA. We offer 9 UG Courses, 4 PG Programs and we also offer research degree programs in ECE and Mechanical Engineering

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

To impart value based technical education and train students to turn out full- fledged engineers in the field of Electronics & Communication Engineering with an overall background suitable for making a successful career either in Industry/Research or higher education i.e, from A.Y 2008-09 with an intake of 60 students. Presently, the intake of the ECE Department is 180 students. The department has faculty strength of 44 well qualified, experienced and dedicated Post graduates with seven Doctorates and some of them are pursuing Ph.D in different streams and 6 supporting staff.

The department encourages students into postgraduate studies and prepares them for leadership roles in research and development. ECE Department is recognised as Research Centre by JNTU Kakinada in A.Y 2019-20. Along with UG, one M.Tech course in Digital Electronics and Communication Systems from A.Y 2014-15 with an intake of 18 students is also offered.

The ECE department has good infrastructure with 8 different labs namely Microwave Engineering lab, Microprocessor Lab, Digital ICs Lab, Communications Lab, Computer Lab, EDC Lab, LIC Lab, DSP & VLSI Lab. All the labs are fully equipped to provide the present day advanced technology. ECE department has three Centre of excellences, Assistive Technology Lab (ATL), National Instruments lab (NI) and Drone Centre of Excellence(DCE)..

The Department has student's chapters like IETE Student Forum and ISTE Student Forum conducts many technical talks, seminars, quiz etc. Every semester at least two guest lectures are being arranged in addition to above activities.

Very recently IEEE student chapter has been started; Students are encouraged to participate in International, National and State level technical contests. Every year our final year students are encouraged to write GATE, CAT, GRE, TOEFL & IELTS exams.

R&D works

I. Acted as Resource Person/Others

1. Dr. N. Padmavathy Acted as reviewer for MISP 2022

2. Dr. Prakash Pareek served as a Reviewer for IEEE Journal of Quantum Electronics

3. Dr. Argha Sarkar served as Reviewer in Journal of Engineering Research and Sciences (JENRS)

4. Dr. Argha Sarkar served as a Reviewer in ICDSA 2022, by School of Mobile Computing and Communication, Jadavpur University, Kolkata, India

II. Faculty Publications in Journals

1. Nimmala Harathi, Kedhaeswara Sairam Pasupuleti, Zhandos Tauanov, Moon-Deock Kim, **Argha Sarkar**, V Navakoteswara Rao, PrGO decorated TiO_2 nanoplates hybrid nanocomposite for augmented NO_2 gas detection with faster gas kinetics under UV light irradiation, Sensors and Actuators B: Chemical, https://doi.org/10.1016/j.snb.2022.131503, 2022 .

2. Ch. Ravikumar, **P. Sivananthamaitrey**, P. Rajesh Kumar, Semi-Blind Watermarking of Digital Images using Evolutionary Algorithms, Design Engineering, 2022.

3. **Manaswi D**, Durga Prasad R, Balaji Naiak M, Murali Krishna Ch and **Durga Prasad D**, Circular Arcs Loaded on Monopole Antenna for 5G Emerging and Ka - band Applications, 2nd International Conference on Communication, Computing & Industry 4.0-2021 (C2I4 2021), 16th & 17th December 2021, 10.1109/C2I454156.2021.9689400, pp.1-5, IEEE Xplore, 2022.

4. **B. Elisha Raju**, **K.Ramesh Chandra**, **Prudhvi Raj Budumuru**, A Two-Level Security System Based on Multimodal Biometrics and Modified Fusion Technique. In: Karrupusamy P., Balas V.E., Shi Y. (eds) Sustainable Communication Networks and Application. Lecture Notes on Data Engineering and Communications Technologies, vol 93. Springer, Singapore. https://doi.org/10.1007/978-981-16-6605-6_2,pp.29-39, 2022.

5. **Abdul Rahaman Shaik, N Durga Naga Lakshmi, Ch V V S Srinivas**, Delivery Robot Using GPS Technology, IEEEICCICA21, IEEE Explore, 2022.

6. **Abdul Rahaman Shaik**, **K. Ramesh Chandra**, **B. Elisha Raju**, **Prudhvi Raj Budumuru**, Glaucoma Identification Based on Segmentation and Fusion Techniques, IEEE International Conference on Advances in Computing, Communication, and Control (ICAC3'21) , IEEE Xplore, 2022.

III. Books Published by Faculty

Bhaskar Reddy, Suneela, **Prakash Pareek**, Pavithra, **Digital Communication Systems**, Notion Press, ISSN: 979-8885696906

Dr. Prakash Pareek
Associate professor
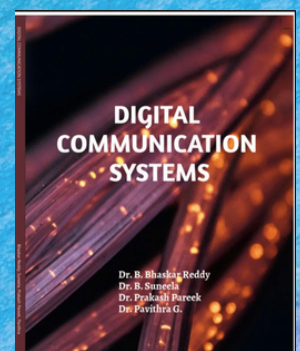
A one day visit to Doordarshan Kendra as a part of industrial trip was very enlightening! This trip gave us an opportunity to learn about high-end communication between satellites..

INDUSTRIALTRIP TO
DOORDARSHAN KENDRA

III ECE

"We were shown the studios and were given explanations for it's way of functioning and designs. We were taken around the Art Station Room, PVR Room, Studio and the Editing Room."

"Glad being a part of this trip to VTPS as, it's one of the biggest power stations in India. This is basically a coal based power plant, where the coal is transported from coal mines to this power station by Railway Goods system. We're allowed to see the main control room and the overall working of the plant, under the guidance of employees."

II ECE

THERMAL POWER STATION
IBRAHIMPATNAM

Design optimisation of multiplier-free parallel pipelined FFT on field programmable gate array

-Dr. G. Prasanna Kumar
Associate Professor

Fast Fourier transform (FFT) is utilised to minimise the complexity of discrete Fourier transform by converting signals from frequency domain to time domain and conversely. Digital signal processing systems like image processing, general filtering, sonar, spread-spectrum communications and convolutions use this FFT operations. Radix-2 decimation in frequency (R2DIF) method is designed to execute an efficient FFT architecture in this study. Each and every state of the FFT stores the input and output the data using the R2DIF method. Also, the complex twiddle factors in FFT are replaced by the proposed uniform Montgomery algorithm. This technique simply performs the shift-add method instead of the multiplication process which also enhances the convergence of the calculation. So, the FFT implementation is done with the help of the proposed method which reduces the usage of chips in the process. Based on this approach, it performs the operation of FFT from 16 points to 1024 points and the performance of this proposed method is compared with existing approaches. Moreover, it does not require expensive dedicated functional blocks and uses only distributed logic resources. The simulation is carried out by the Xilinx platform using Verilog coding. The proposed design outperforms conventional methods in terms of less usage power and high speed.
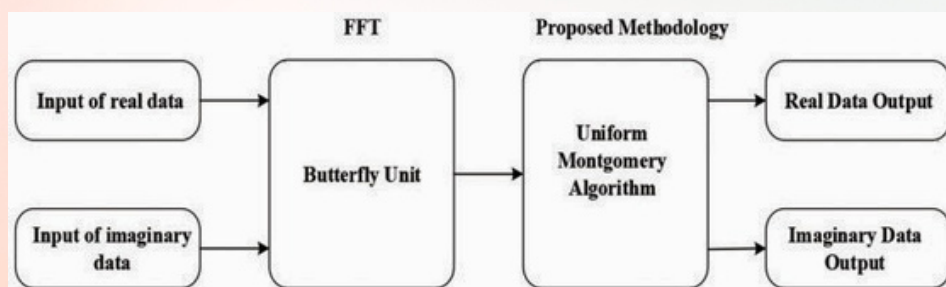

Figure 1: Work flow of proposed methodology

Figure 2: Register transfer level (RTL) view of single butterfly unit and Uniform Montgomery processor
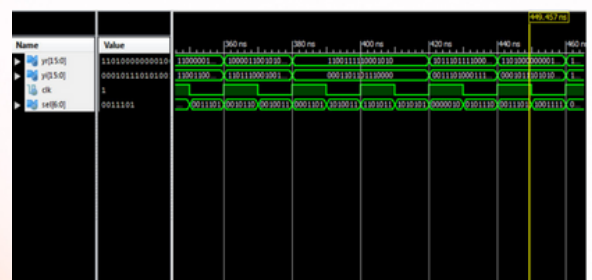

Figure 3: Output of FFT design based on UMA

This proposed method based on UMA is to replace the twiddle factor design of different point radix-2 FFT which is mainly used in future communication standards and modern data-intensive signal processing applications. This algorithm replaces the complex multipliers in FFT. The implementation was coded in Verilog and implemented in the Xilinx platform. Execution time, power, speed and area occupied by each design were taken as parameters to compare the performance of the proposed scheme. Instead of using more functional blocks, the proposed design makes simple, flexible, fast and low-cost chip to enhance the performance of the proposed FFT processor design. The proposed FFT design achieves 12.91% speed and 59.95% less power usage when compared to the modern methods. Better processing speed is delivered by the proposed FFT design.

GATE OXIDE OVERLAP ONTO SOURCE HETEROJUNCTION TUNNEL FIELD EFFECT TRANSISTOR (GOS HTFET)

Dr. B. V. V.Satyanarayana
Associate Professor

Moore's law: As the number of transistors placed increase, the power consumption increases due to more chip activity. Temperature increases and restricts the chip activity. New power optimization techniques required. Voltage scaling is required power efficiency, but high leakage current and low speed of operation

Design of Low Power GOS HTFET

The proposed GOS HTFET with reduced SS and less leakage by overlapping of gate oxide onto source region.Scaling of GOS HTFET is possible because of lower subthreshold swing and high ON state current with less leakage. There are two possibilities of transistors: one with source as P-type and drain as N-type regions and another one with source as N-type and drain as P-type regions.There are two types GOS HTFETs: N-type and P-type.NHETT structure consists of P-source region and N-drain regions and gate oxide layer overlapped on to source. PHETT structure consists of N-source region and P-drain regions and gate oxide layer overlapped on to source. Low band gap materials such as Ge or SiGe is used for the source and drain regions.
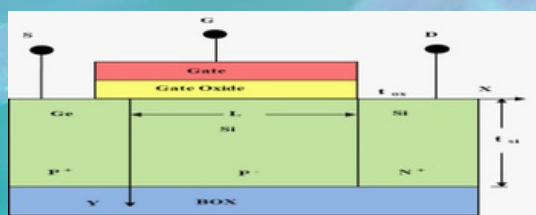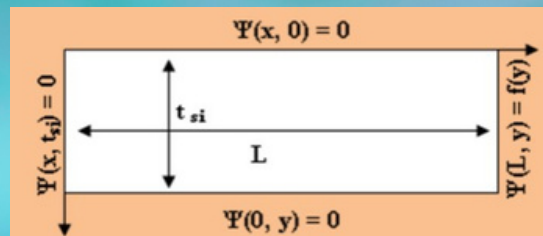
N-Type GOS HTFET
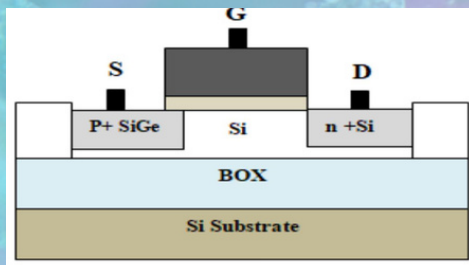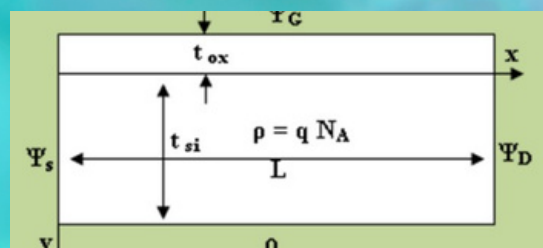

P-Type GOS HTFET
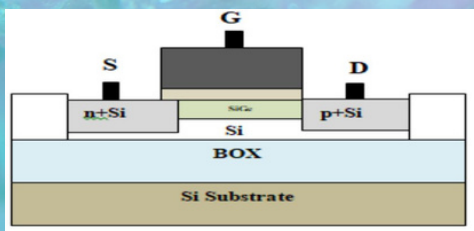
Modelling of GOS HTFET

Cross section of SOI NHETT

Homogeneous boundary conditions

Boundary conditions potential distributions

Features of GOS HTFET

- Gate oxide overlapping
- Low bandgap materials
- High BTBT
- Tunnel current
- Less leakage current
- Low subthreshold swing
- Miller capacitance
- SiGe is mostly used for heterojunctions.
- This offers opportunities in mixed-signal circuit and analog IC design and manufacture for high temperature applications.
- The subthreshold swing of the proposed device is 16mV/decade.
- ION (A/μm) = 0.65 x 10-3.
- IOFF (A/μm) = 1.2 x 10-15.
- ION/IOFF = > 1011.

Heterojunction tunneling transistor with gate oxide overlap onto source is modelled by using 2D Poisson's equation and is given as

$$\frac{\partial^2 \psi(x,y)}{\partial x^2} + \frac{\partial^2 \psi(x,y)}{\partial y^2} = \frac{qN_c}{\varepsilon_{si}}$$

Using above equations, the 2D solution for surface potential of proposed HETT is as follows

$$\psi_{Surface} = \sum_{n=0}^{\infty} \frac{A\sinh(h_n x)}{\sinh(h_n L)}\sin(h_n y)$$
$$+ \sum_{n=0}^{\infty} \frac{B\sinh(h_n(L-x))}{\sinh(h_n L)}\sin(h_n y)$$
$$+ \psi_G\left[1 - \frac{y}{t_{si} + \gamma t_{ox}}\right]$$

where

$$A = \frac{2}{t_{si}}\int_0^{t_{si}}\left[\phi_S - \psi_G - y\frac{\psi_G}{t_{si} + \gamma t_{ox}}\right]\sin(h_n y)dy$$

$$B = \frac{2}{t_{si}}\int_0^{t_{si}}\left[\phi_D - \psi_G - y\frac{\psi_G}{t_{si} + \gamma t_{ox}}\right]\sin(h_n y)dy$$

11

Simulation of GOS HTFET

The SOI substrate with the BOX of HTFET has the Si layer as the top with back substrate. A high-K dielectric layer such as HfO2 formed on the SOI substrate region. The channel below the gate region consists of the source region in SiGe or Ge layer while drain region in the Si layer. A region of Ge/Si heterojunction created in between source and drain regions.
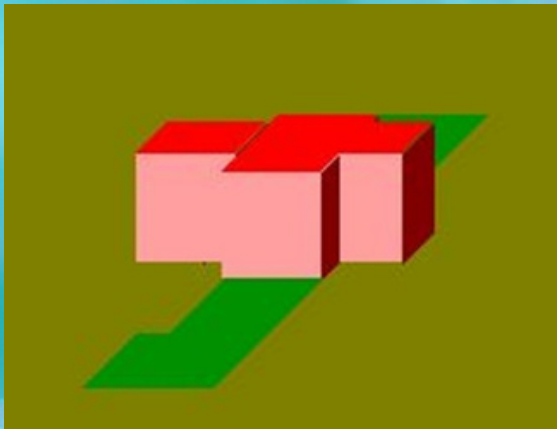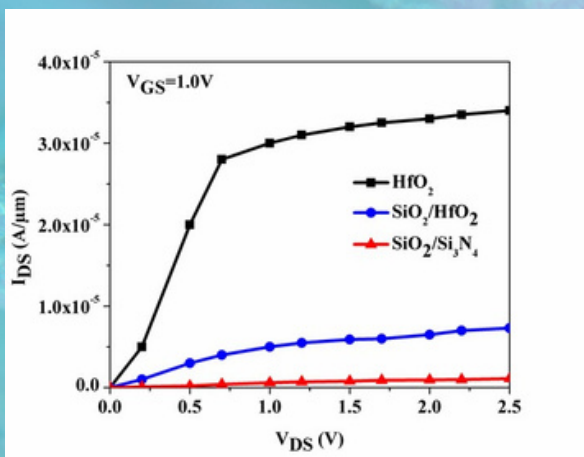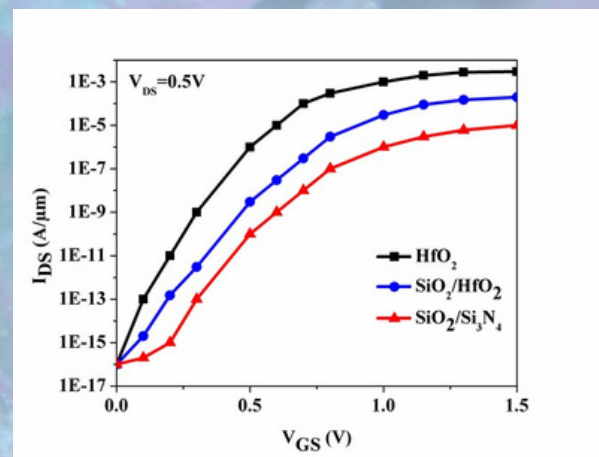

3D Structure of Simulated NHETT
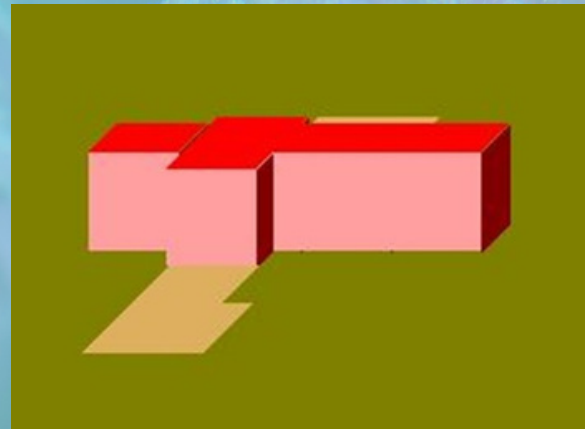
3D Structure of Simulated PHETT

Characteristics of GOS HTFET

Proposed HTFET Output Characteristics

Proposed HTFET Transfer Characteristics

SiO2 is used as gate oxide in the traditional devices, but scaling reduces the oxide thickness to increase the drive current by increase gate capacitance.The device is further scaled down i.e. below 2nm, leakage current increases due to increased tunneling and the power consumption of the device rises. In order to scale the HTFET device further and increase the performance characteristics, high-k dielectric materials are used instead of SiO2.

These materials increase gate capacitance with very low leakage current. The threshold voltage of HTFET device depends on oxide thickness, channel length and gate to drain voltage.The subthreshold swing of GOS HTFET is 16mV/dec calculated from drain output characteristics.The implementation of high-K gate dielectrics is one of several strategies developed to allow further miniaturization of electronic components, referred as extending Moore's Law.

Conclusions

The scaling of MOSFET is restricted due to the lower limit of subthreshold swing which is not less than 60mV/decade and hence these devices are not further scaled down for ultra-low power systems.The GOS HTFET is modelled, simulated and validated and can be used in different low power memory applications. The device is exhibiting lower subthreshold swing, high ON state drive current, increased ON-OFF current ratio with improved Miller capacitance. In future, the digital logic can be implemented by the combination of both CMOS and HTFET technologies. The HTFET technology is best suitable for designs require low leakage current whereas CMOS technology for better performance.

Circuit of Life
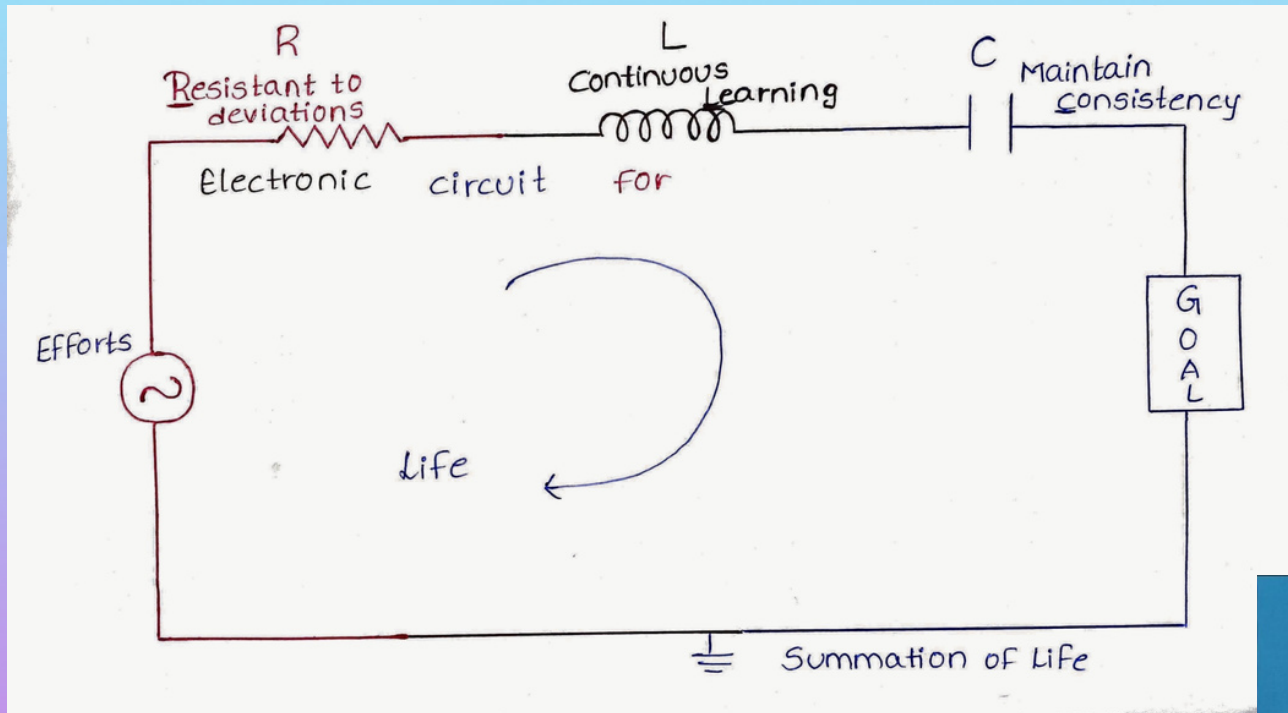
Being a good Engineer means being aware of the limitations of real components compared to their ideal abstractions.
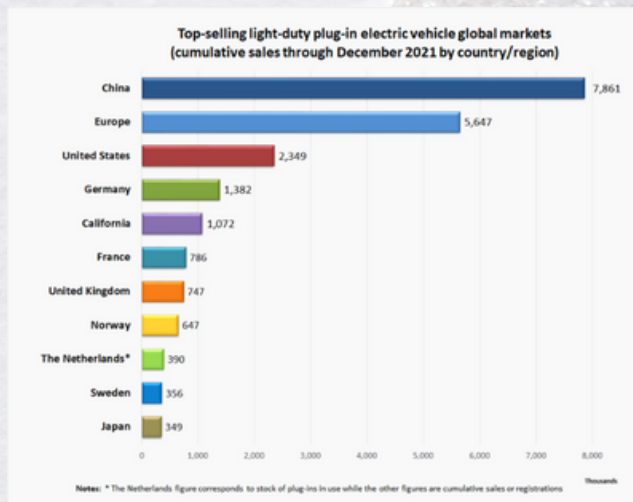
Josyula Venkatramayya
Lab Technician
ECE Department

Student Research Activities

1. *K. B. N. Sai Sumanjali, K. V. Sai Vinay, M. M. Pasha, M. Pavani Sujji, N. Syam Kumar and Prudhvi Raj Budumuru, "Arduino Based Smart Glove for Visually Impaired," 2021 5th International Conference on Electronics, Communication and Aerospace Technology (ICECA), pp. 267-271, doi: 10.1109/ICECA52323.2021.9676164, IEEE Xplore, 2022 SCOPUS.*

2. *R. Prem Chand, V. Bavya Sri, P. MahaLakshmi, S. Santosh Chakravathi, O. D. M. Veerendra and Ch Venkateswara Rao, "Arduino Based Smart Dustbin for Waste Management during Covid-19," 2021 5th International Conference on Electronics, Communication and Aerospace Technology (ICECA), 2021, pp. 492-496, doi: 10.1109/ICECA52323.2021.9676003. IEEE Xplore, 2022 SCOPUS*

ELECTRIC VEHICLES:
The Revolutionary Aspect

- M.N.Nirupam
19PA1A0496

An electric vehicle is a one powered by an electric motor rather than a traditional petrol/diesel engine. This electric motor is powered by rechargeable batteries that can be charged by common household electricity. EV's are either partially or fully powered on electric power. Electric vehicles have environmentally friendly as they use little or no fuels. William Morrison, from Des Moines, Iowa, creates the first successful electric vehicle in the U.S. His vehicle is little more than an electrified wagon, but it sparks an interest in electric vehicles.

Top-selling light-duty plug-in electric vehicle global markets
(cumulative sales through December 2021 by country/region)

Country/Region	Sales (Thousands)
China	7,861
Europe	5,647
United States	2,349
Germany	1,382
California	1,072
France	786
United Kingdom	747
Norway	647
The Netherlands*	390
Sweden	356
Japan	349

Notes: * The Netherlands figure corresponds to stock of plug-ins in use while the other figures are cumulative sales or registrations

China is known as biggest market for electric vehicles in the world. Electric vehicles accounted for almost 13.3 percent of the Chinese vehicle market, China has nearly 2.22 million charging slots across the country. EV's have the benefit of flexible charging. Since the electric grid is in close proximity to most locations where people park, they can charge overnight at a residence as well as at a multi-unit dwelling, workspace, or public charging station when available.

Most electric vehicles use Lithium-ion batteries which have higher energy density, longer life span and higher power density than most other practical batteries. Complicating factors include safety, durability, thermal breakdown and cost. Li-ion batteries should be used within safe temperature and voltage ranges in order to operate safely and efficiently Increasing the battery's lifespan decreases effective costs. One technique is to operate a subset of the battery cells at a time and switching these subsets.

Instead of recharging EVs from electric socket, batteries could be mechanically replaced at special stations in a few minutes. Batteries with greatest energy density such as metal-air fuel cells usually cannot be recharged in purely electric way. Instead, some kind of metallurgical process is needed, such as aluminium smelting and similar. Electric vehicles may have shorter range compared to vehicles with internal combustion engines, which is why large electric ships generally cannot cross oceans.

Through the Strokes...
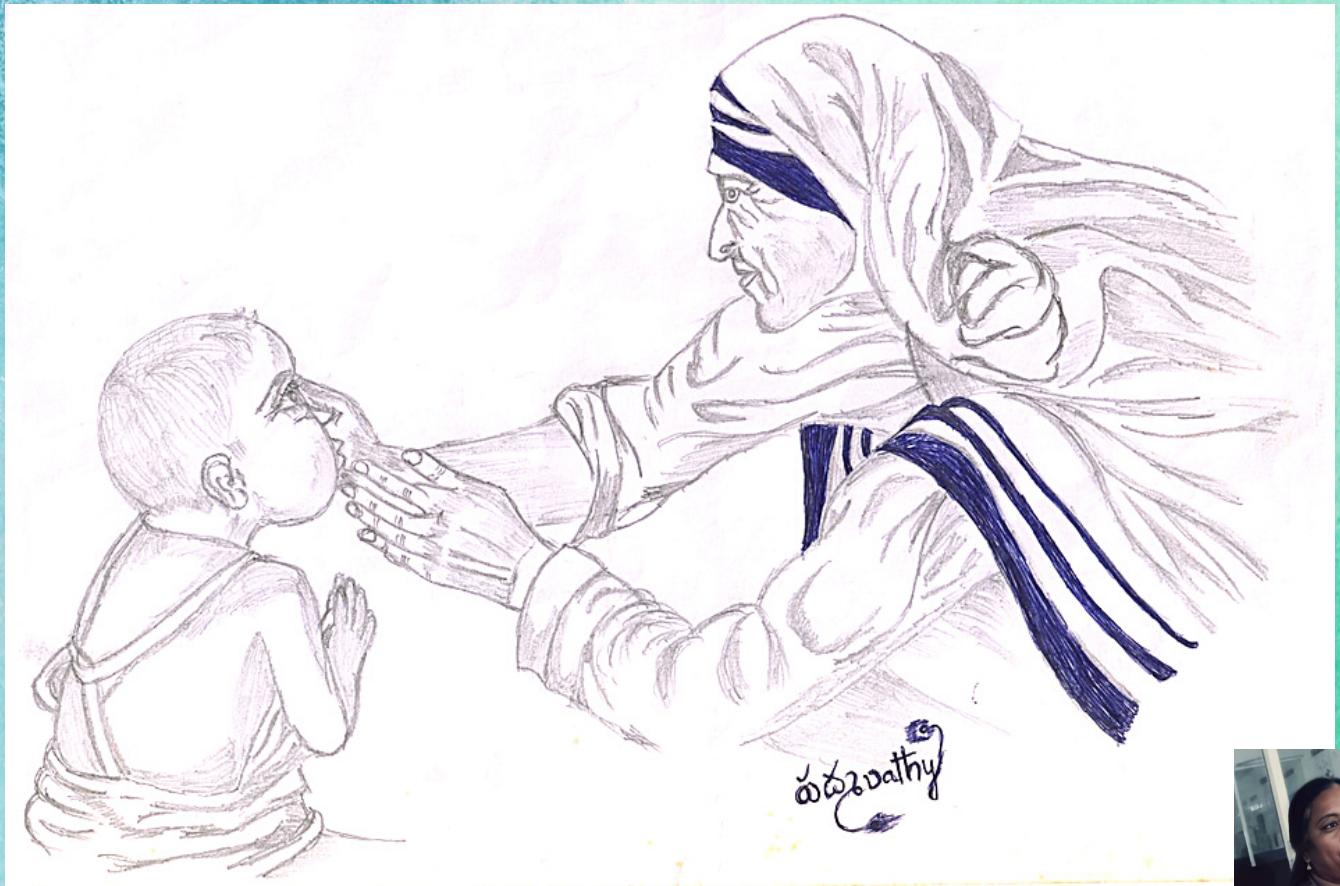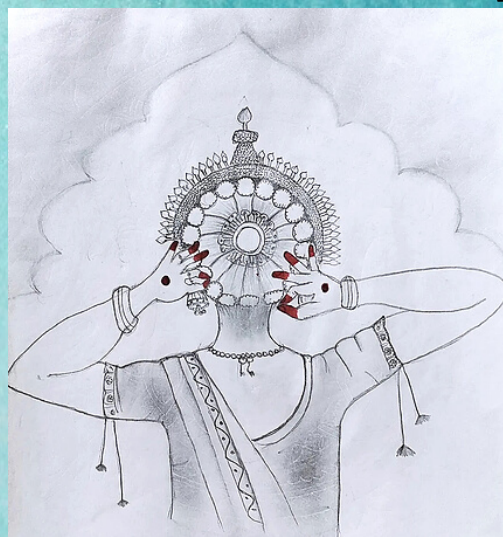
-Dr. N. Padmavathy, Ph. D (IIT - KGP)
Professor and Head
Department of ECE

K.Sai Vamsi
20pa1a0467

Dr. B.V RAJU

-G. Uday Kiran
20PA1A0451

Being Consistent Changed My Life !

-B.Jyothi Swaroop
20PA1A0412

From childhood I used to see everyone doing their best in either of the activities like Studies, Extra Curriculars, sports, etc. But I wasn't the best in anything and performed equally in all. With some extra effort, consistency and Focus, I thought I could be the best.

YouTube was something which I tried. It didn't workout for a few days but I've heard that being consistent could definitely bring the change. So I started being regular in YouTube with my little knowledge of Video making and Content Creation. It worked out! I did it.

Being a creator in YouTube improved my speaking, editing and content creation skills. A few of my videos are about the college, some technical information including a few guidance videos. These had a great reach among teenagers that increased my spirit!

NEW VIDEOS EVERY WEEK @ **SWAROOP** VITB — GUIDANCE, LEARNING, VLOGS — Always Keep Smiling

Is BlockChain The Future!?

To make it simple for everyone,
let's go back in time to when WEB 1.0 was introduced. In WEB 1.0 there were only websites like Wikipedia,etc. Where people from all over the world have access to the data. Then came the WEB 2.0 where Big Companies like FaceBook, Google, Microsoft to provide better experience for users they started storing our data.

To make it even more secure, now with the help of Blockchain Technology and Artificial Intelligence, WEB 3.0 is introduced. Web 3.0 is the next stage of the web evolution that would make the internet more efficient through the power of AI systems that could run smart programs to assist users. Web 3.0 is meant to "automatically" interface with systems, people and home devices.

Now you might think what's the use of Blockchain and what it actually is, Isn't it? So with the help of Blockchain technology your data is entirely secure and your data is spread into millions of nodes in the form of blocks, No one can hack or access your details except you. Hence Blockchain is playing a major role in Crypto Currencies and NFTs.

-B.Jyothi Swaroop
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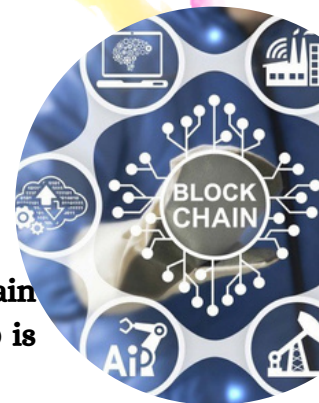
Student Corner

A. Sai Pavan Teja, A. Kumar under the Mentorship of **Dr. Argha Sarkar** (Dept of ECE, Vishnu Institute of Technology) won the **first prize** in **"Technovation 2021"** held during **21-22 January, 2022**.

Title of Innovation: Intelligent systems for Automatic Supervision of Temperature and Social Distancing in Institutions/Organizations


Sports

"I was very happy to represent Vishnu football team Captain in JNTUK ICT cum university Selection. In the history of Vishnu Institute Of Technology, it was our first university selection in the event of Football. Enjoyed with the team of 20 members from various affiliated colleges in South Zone Football Tournament at Kerala."
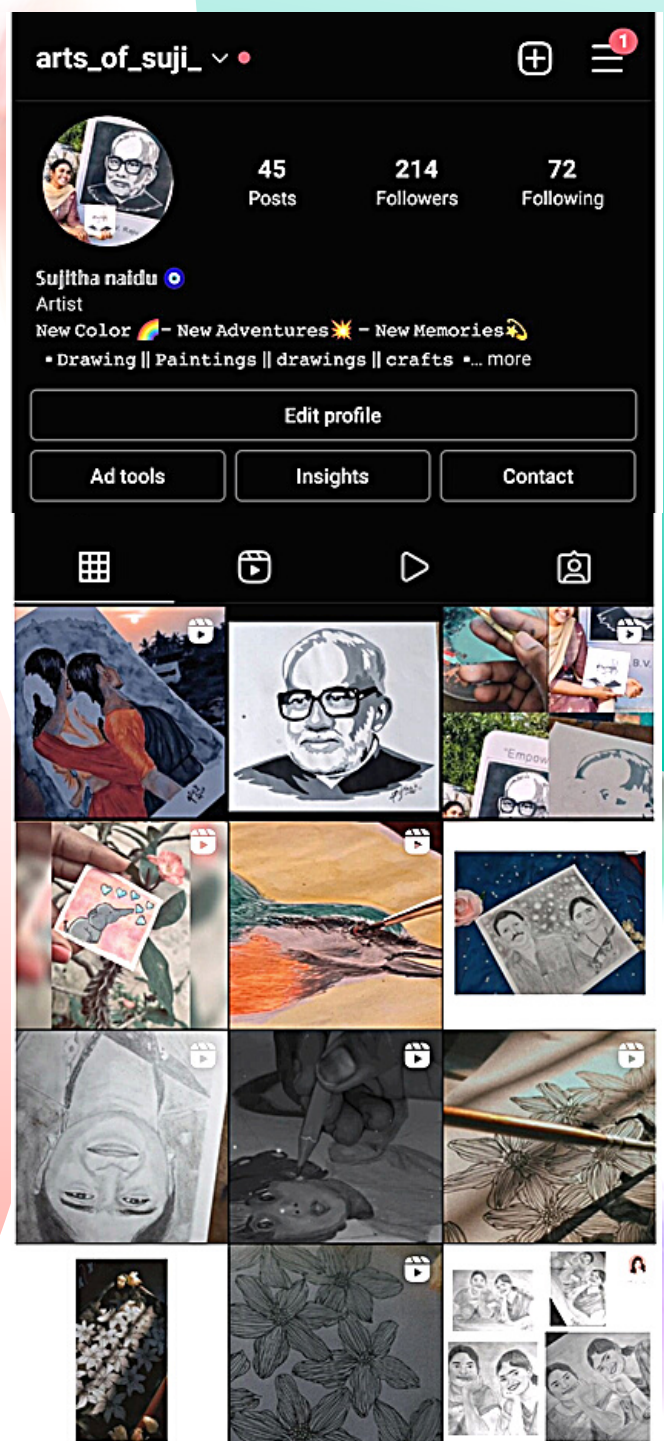

Adabala John Prasad
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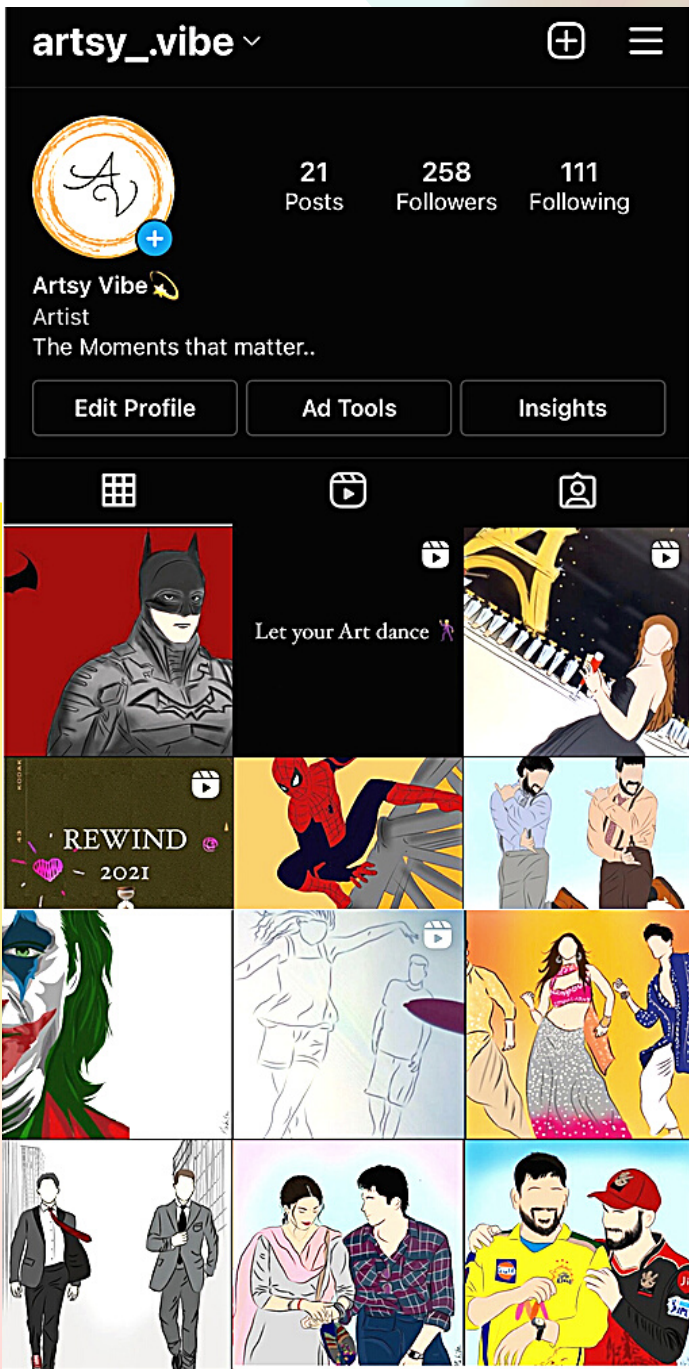
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The Showcase

"I am a self-taught artist. I always had a thought about creating my own art blog. I make arts with a waste materials like plastic bottles , news papers and wires . And I love to draw portraits and paintings . At present I was working on wpap arts ."

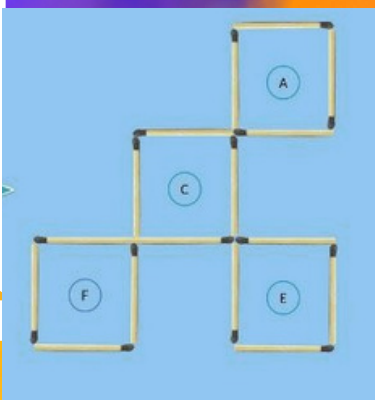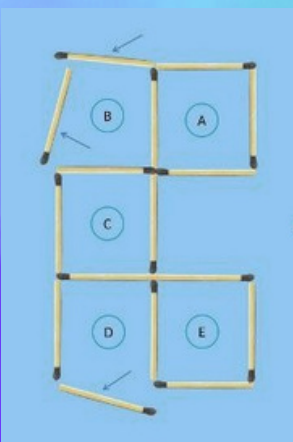**Instagram Profile**: *arts_of_suji*

-K. Sujitha
20PA5A412

Puzzle 1:

The grid below has a specific pattern that determines the numbers in each box. What are the missing four numbers?

9	6	2	1
15	12	8	7
21	18	14	?
27	24	20	19
?	30	?	?

FUN ZONE

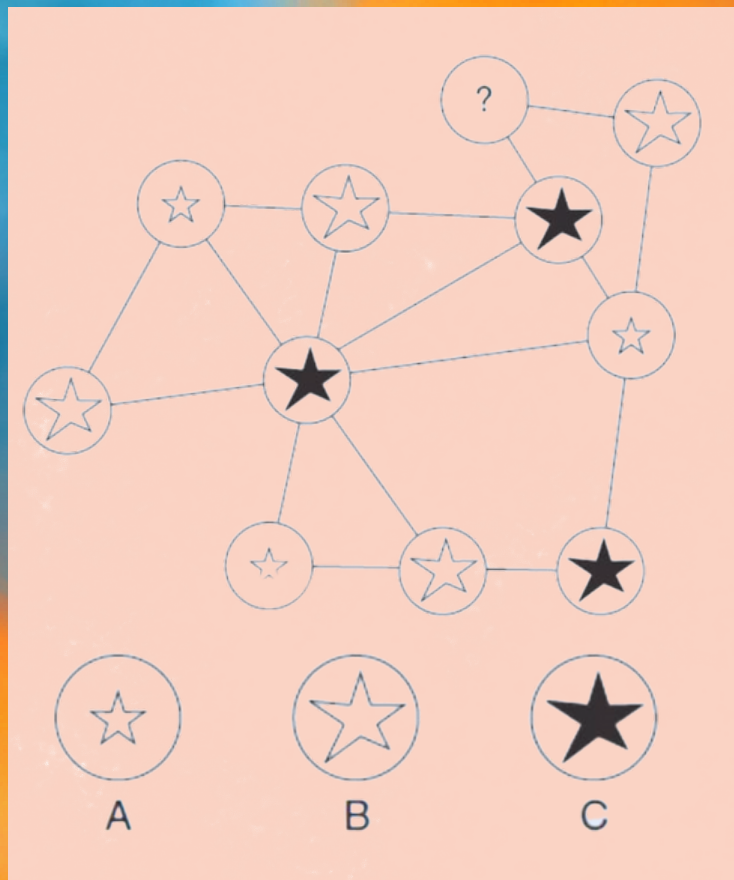## SOLUTION:

For the puzzle in previous Edition - Jan 2022(Page: 17)

Puzzle 2:

Find the missing part!

Alumni Experiences

"Hi ,This is Kiranmai of ECE department, 2015 passed out from Vishnu Institute of Technology , Bhimavaram. I am very glad to share my experiences from my days at college. Every achievement and mistake taught me about discipline and knowledge. We had good rapport with our staff with whom we can interact freely, which paved a way for an excellent learning environment .We were encouraged to give seminars, paper presentations and attend industrial training which helped us to develop great communication skills and explore new arenas. The projects that we had done during our academics were so good and and gained some hands-on experience. Apart from learning, I had a great time with my friends by participating in extra curricular activities. In the view of placements, we had an extraordinary placement training program included mock interviews, day-to-day assessments with regular monitoring. The result being I got selected in two companies during the college placements. I thank each and everyone for making me who I'm."

Kiranmai
Batch 2011-15
Consultant
Virtusa

"Hi friends. I am Avinash. Firstly, I would like to congratulate you for choosing VIT for your career and I want to tell few points about VIT and how it helped me to get succeed in my life. I am currently working as DC Consultant at Deloitte. I studied in VIT from 2012-2016 in ECE department and I got a fair percentage in academics and got placed in TCS with the help of placement team. We have a good percentage of students getting placed over the last few years. Some of them are placed in MNC's with good packages. I got placed in TCS with a fair package, now my life is very good this is all because of my college it was in the first place to help me to face all the struggles in my life and to be in this position. So, I want to thank all the faculty members who helped me a lot, I think you guys will utilize the all the facilities in the college to be successful in your life."

Avinash
Batch 2012-16
DC Consultant
Deloitte

PLACEMENTS - 2022

Total offers: 399

Students placed : 161

7.5-10 LPA: 12 Offers

5-7.5 LPA: 25 Offers

4 - 5 LPA: 226 Offers

Upto 4 LPA: 399 Offers

Editorial Board

Mr.B.Prudhvi Raj
Assistant Professor

Mrs.D.Manaswi
Assistant Professor
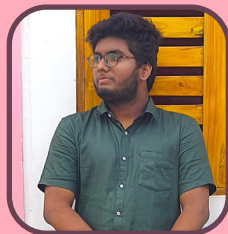
Student Members

III YEAR

V.Mahita
19PA1A0495

V.Bavya Sri
19PA1A04H3

M.N.Nirupam
19PA1A0496

K.Subbaramireddy
19PA1A0484

M.Sriram
19PA1A04B0

N.Syamkumar
19PA1A04B7

N.Sandeep
19PA1A0B6

M.Jagadeesh
19PA1A04A9

Shaik Ahmed
19PA1A04E9

B. Uday
19PA1A0420

A. Ajay
19PA1A0410

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Padma Bhushan Dr.B.V.Raju
&
Smt. B. Seetha Devi

"Serve humanity in your Capacity"